

This listing of claims will replace all prior versions, and listings, of claims in the application:

Claims 1-17 (canceled)

Claim 18 (currently amended): A solid-state image sensing apparatus comprising:

an image sensing area in which a plurality of pixels are two-dimensionally arrayed;

a plurality of output channels;

a first driving mode ~~[[for reading]]~~ in which pixel signals of pixels in the image sensing area are read out, wherein the read-out pixel signals are output to at least one output channel selected from among the plurality of output channels ~~[[the first driving mode selects a first set of output channels]];~~

a second driving mode ~~[[for reading]]~~ in which pixel signals of ~~[[pixels]]~~ odd-numbered columns and pixel signals of even-numbered columns arrayed in the same row in the image sensing area are read-out, wherein the read-out pixel signals are output to a plurality of output channels selected from among the output channels, and wherein the read-out pixel signals of odd-numbered columns and the read-out pixel signals of even-numbered columns are output to different ones of the selected output channels so as to have different phases; and ~~[[wherein the second driving mode selects a second set of output channels;]]~~

a control circuit which sets driving mode to one of the first driving mode and the second driving mode based on an externally input ~~[[control]]~~ signal, wherein the input ~~[[control]]~~ signal may be freely set,

27 wherein the number of output channels to which the
28 pixel signals are output in the first driving mode and
29 ~~[[set is different from]]~~ the number of output channels to
30 which the pixel signals are output in the second [[set]]
31 driving mode are different.

1 Claim 19 (new): The solid-state image sensing apparatus
2 according to claim 18, further comprising line memories
3 which temporarily store selected and read-out pixel signals
4 of pixels,

5 wherein the control circuit is arranged between the
6 pixels and the line memories and is a transfer switch in
7 which a common control signal is input in every other
8 column.

1 Claim 20 (new): The solid-state image sensing apparatus
2 according to claim 18, wherein the phase shift between the
3 pixel signals of the odd-numbered columns and the pixel
4 signals of the even-numbered columns is 180 degrees.

1 Claim 21 (new): The solid-state image sensing apparatus
2 according to claim 18,

3 wherein the image sensing area is provided with a
4 color filter in Bayer matrix corresponding to the pixels,
5 and

6 in the second driving mode, pixel signals of pixels in
7 the same color phase among color phase codings defined by
8 the color filters are output from the same output channels.

1 Claim 22 (new): The solid-state image sensing apparatus
2 according to claim 18, wherein in the first and second

3 driving modes, there is a channel which can be used in
4 common.

1 Claim 23 (new): The solid-state image sensing apparatus
2 according to claim 18, wherein in both the first and second
3 driving modes, pixel signals of pixels from $m \times n$ pieces in
4 the image sensing area are output wherein m and n are
5 integers.